

Fig. 1
(Prior Art)

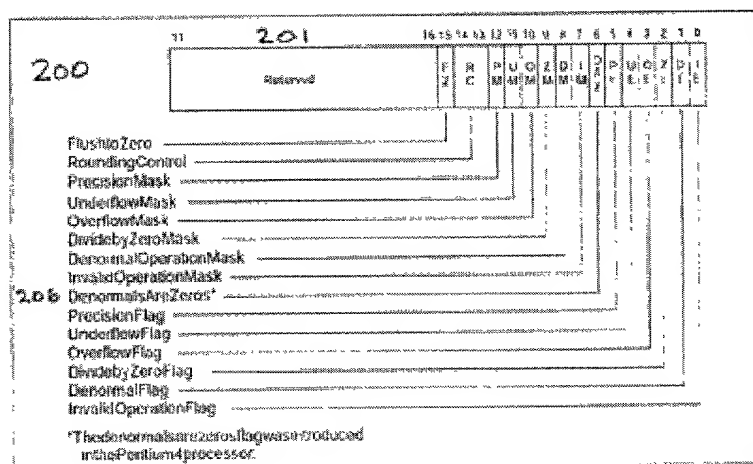


FIGURE 2.

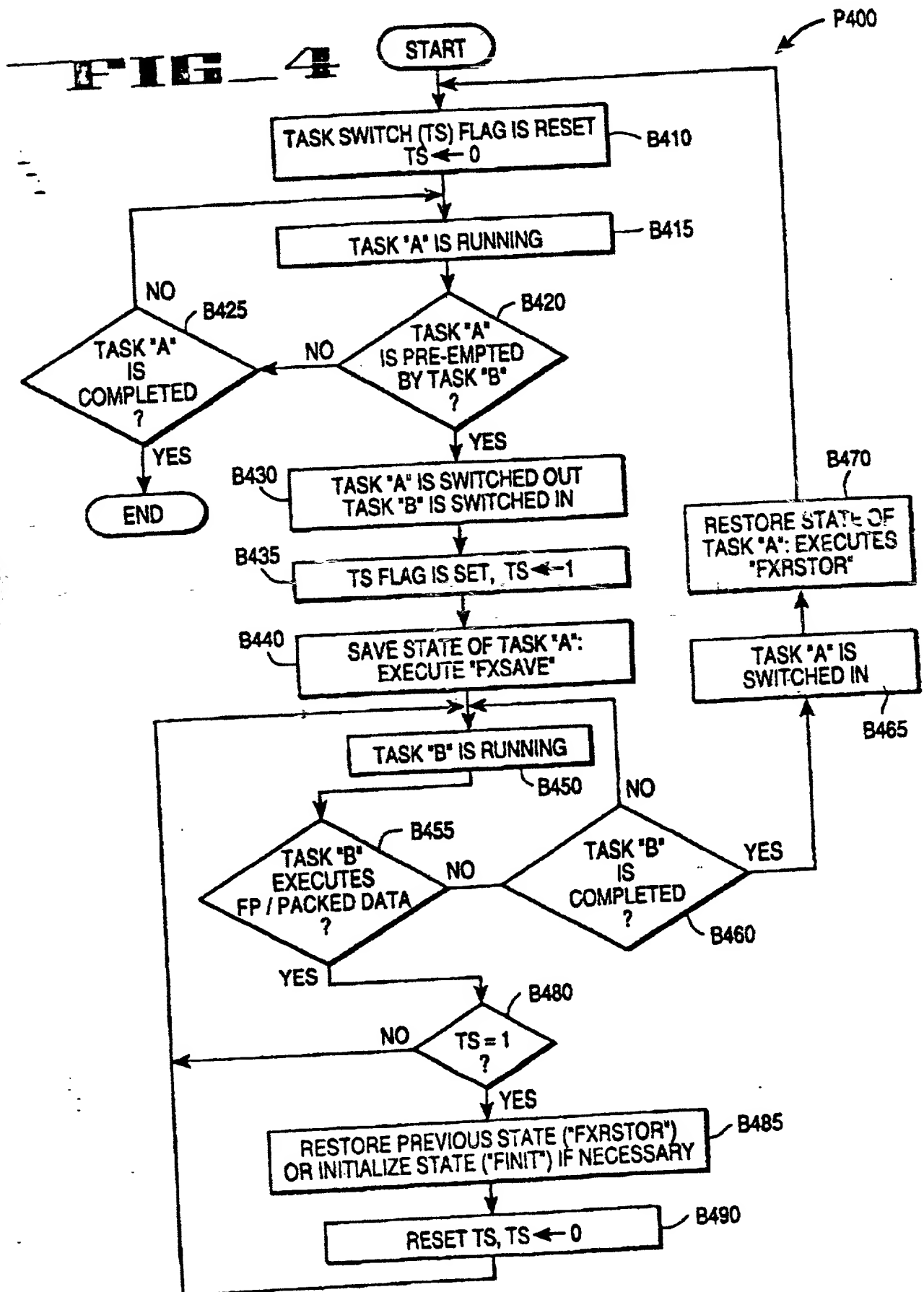
300

302

301

15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00	
Rsvd		CS		IP				FOP		FTW		FSW		FCW		0
MXCSR_MASK				MXCSR				Rsvd		DS		DP				16
Reserved						ST0/MM0										32
Reserved						ST0/MM1										48
Reserved						ST0/MM2										64
Reserved						ST0/MM3										80
Reserved						ST0/MM4										96
Reserved						ST0/MM5										112
Reserved						ST0/MM6										128
Reserved						ST0/MM7										144
								XMM0								160
								XMM1								176
								XMM2								192
								XMM3								208
								XMM4								224
								XMM5								240
								XMM6								256
								XMM7								272
								Reserved								288
								Reserved								304
								Reserved								320
								Reserved								336
								Reserved								352
								Reserved								368
								Reserved								384
								Reserved								400
								Reserved								416
								Reserved								432
								Reserved								448
								Reserved								464
								Reserved								480
								Reserved								496
								Reserved								512

FIGURE 3



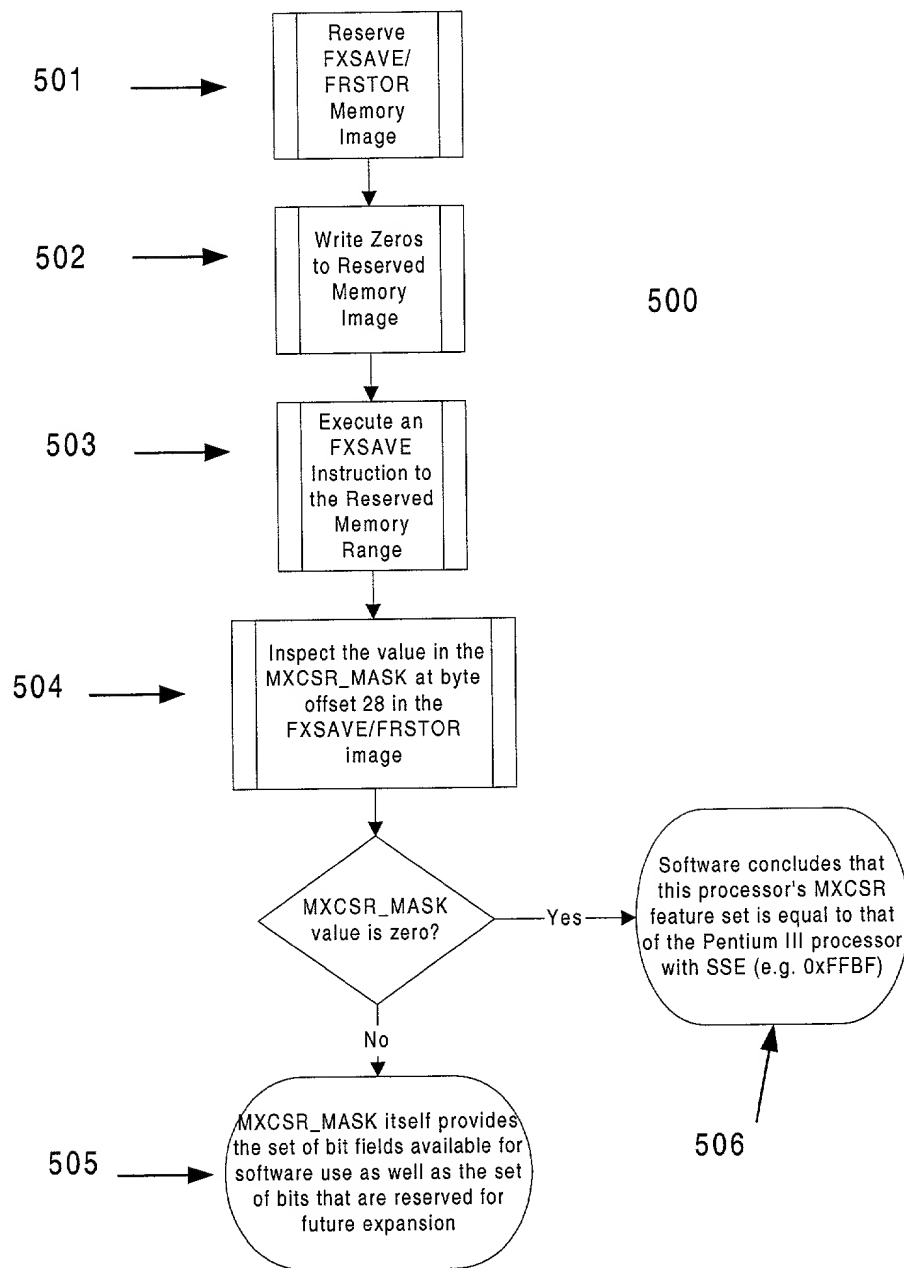


FIGURE 5.

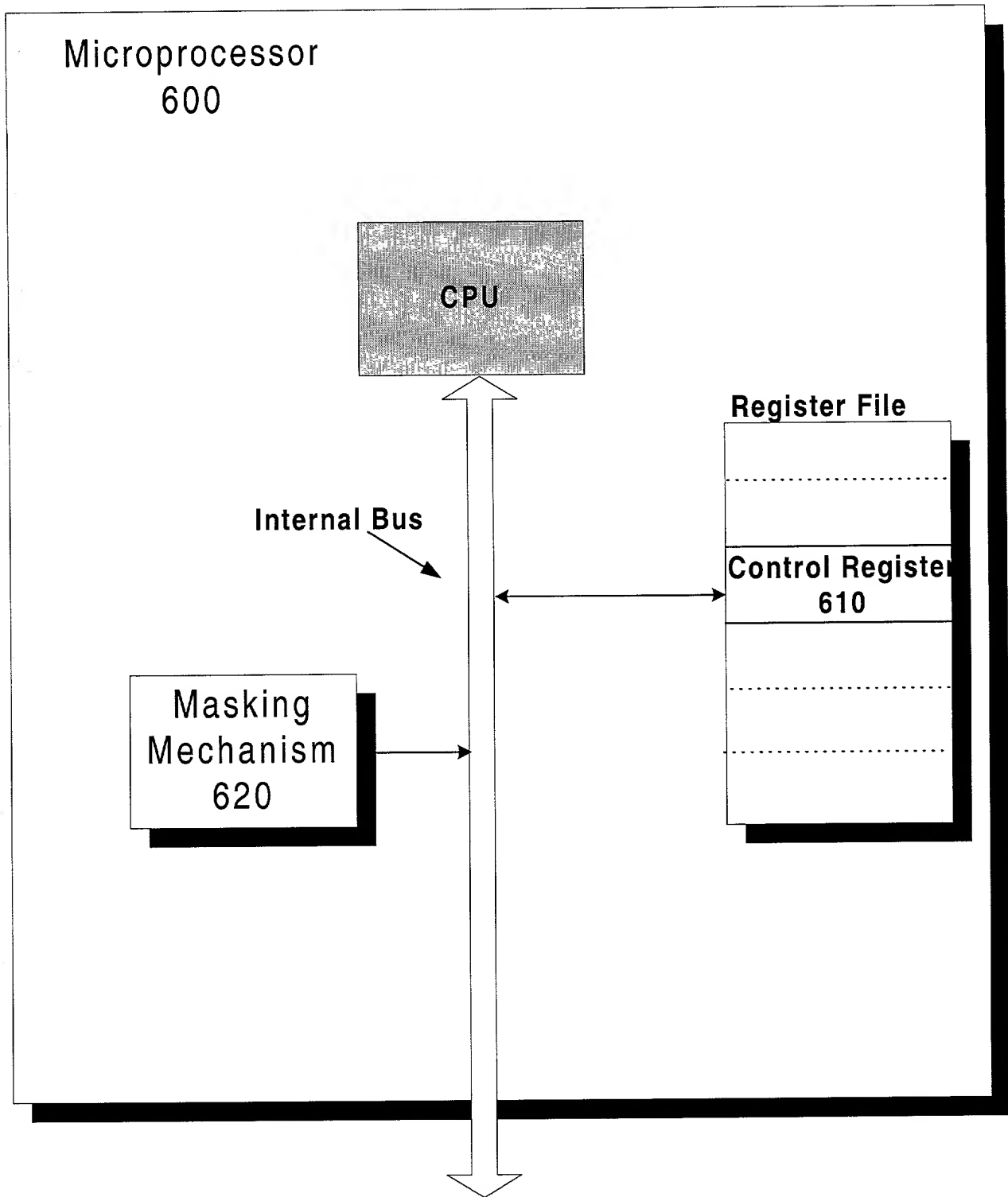


FIGURE 6.